

ECE/CS 552: Introduction to Computer Architecture
(Fall 2007)

SYLLABUS FOR THE FINAL

Date: December 17, 2007
Location: 2317 Engineering Hall
Time: 12:25 – 2:25 pm

The final will focus primarily on topics discussed in class after the midterm. You are expected to be familiar with the material covered before the midterm. The final will be open text book and notes. The following is a representative (but not exhaustive) list of topics that will be covered on the final.

- Pipelining: Data hazard, Branch hazard, Delayed branching, Datapath and Control Logic for Pipelined Design, Exception handling.
- Arithmetic: Multiplication, Division, Floating Point.
- Memory: Interleaving, Caches, Virtual Memory.

In terms of chapters from Patterson and Hennessey the exam will cover pages 176–225, 370–432, and 468–545. Topics discussed in class over and above the material in the text will also be covered in the final. Topics in text which were not covered in class will not be part of the exam.